

**Specification Amendment:**

Please amend the section entitled Cross Reference to Related Applications as follows:

[0001]

This application claims the benefit of U.S. Provisional Application No. 60/252,724 entitled "METHOD AND APPARATUS FOR STORAGE I/O WITH FULL-DUPLEX ONE-TIME BLOCK I/O TRANSFER AND ADAPTIVE PAYLOAD SIZING," filed November 22, 2000, and is related to U.S. Patent Application No.           10/037,168           entitled "METHOD AND SYSTEM FOR PLESIOSYNCHRONOUS COMMUNICATIONS WITH NULL INSERTION AND REMOVAL" (Attorney Docket No. 371795947280802US); U.S. Patent Application No. 10/045,393, entitled "METHOD AND SYSTEM FOR TRANSITION-CONTROLLED SELECTIVE BLOCK INVERSION COMMUNICATIONS" (Attorney Docket No. 371795947280807US); U.S. Patent Application No. 10/035,591 entitled "COMMUNICATIONS ARCHITECTURE FOR STORAGE-BASED DEVICES" (Attorney Docket No. 371795947280808US1); U.S. Patent Application No. 10/036,135 entitled "METHOD AND SYSTEM FOR PACKET ORDERING BASED ON PACKET TYPE" (Attorney Docket No. 371795947280813US); U.S. Patent Application No. 10/036,794 entitled "METHOD AND SYSTEM FOR HOST HANDLING OF COMMUNICATIONS ERRORS" (Attorney Docket No. 371795947280814US); U.S. Patent Application No. 10/045,606 entitled "METHOD AND SYSTEM FOR DYNAMIC SEGMENTATION OF COMMUNICATIONS PACKETS" (Attorney Docket No. 371795947280815US); U.S. Patent Application No. 10/045,348 entitled "METHOD AND SYSTEM FOR ASYMMETRIC PACKET ORDERING BETWEEN COMMUNICATIONS DEVICES" (Attorney Docket No. 371795947280816US); U.S. Patent Application No. 10/053,461 entitled "METHOD AND SYSTEM FOR COMMUNICATING CONTROL INFORMATION VIA OUT-OF-BAND SYMBOLS" (Attorney Docket No. 371795947280817US); U.S. Patent Application No. 10/045,625 entitled "METHOD AND SYSTEM FOR INTEGRATING PACKET TYPE INFORMATION WITH SYNCHRONIZATION SYMBOLS" (Attorney Docket No. 371795947280818US); U.S. Patent Application No. 10/035,911 entitled "METHOD AND SYSTEM FOR NESTING OF

COMMUNICATIONS PACKETS" (Attorney Docket No. ~~3717959472808~~19US); U.S. Patent Application No. 10/045,297 entitled "COMMUNICATIONS ARCHITECTURE FOR MEMORY-BASED DEVICES" (Attorney Docket No. ~~3717959472808~~12US); U.S. Patent Application No. 10/045,600 entitled "METHOD AND SYSTEM FOR DC-BALANCING AT THE PHYSICAL LAYER" (Attorney Docket No. ~~3717959472808~~20US); and U.S. Patent Application No. 10/045,601 entitled "MULTISECTION MEMORY BANK SYSTEM" (Attorney Docket No. ~~3717959472808~~21US, which are all hereby incorporated by reference in their entirety.